



#108/1-1603
V. JQU

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q53838

Katsuyuki SUZUKI

Appln. No.: 09/357,990

Group Art Unit: 2133

Confirmation No.: 9989

Examiner: D. Chung

Filed: July 21, 1999

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Technology Center 2100

For: INITIALIZING/DIAGNOSING SYSTEM IN ON-CHIP MULTIPROCESSOR SYSTEM

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated October 23, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Please enter the following amended claims:

B1

1. (Twice amended) A semiconductor chip comprising:
a plurality of first elements each of which diagnoses itself; and
a second element which inputs diagnosis results from said first elements, discriminates each of said diagnosis results into a majority group or a minority group, and determines said first element whose diagnosis result is in said minority group as a faulty first element.

B2

12. (Twice amended) A method which is performed in a semiconductor chip including a plurality of first elements, comprising:
diagnosing said first elements each by itself;